

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Kitsukawa et al.

Serial No. Continuation of 10/120,872

Filed: Herewith

For: System with Meshed Power and Signal
Buses on Cell Array

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Group Art Unit: Unknown

Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT

Commissioner For Patents
Washington, D.C. 20231

Dear Sir:

In compliance with the duty of disclosure under 37 CFR §1.56, and in accordance with the practice under 37 CFR §1.97 and §1.98, the Examiner's attention is directed to the documents listed on the enclosed modified Form PTO-1449. No inference should be made that the cited references are in fact material, are in fact prior art, or that no better art exists. The cited patents and documents are listed in chronological order and are not in any order based on their pertinence.

Pursuant to 37 CFR §1.98(d), no copies of the cited references are enclosed since the references were provided in the parent application, U.S. Serial No. 10/120,872.

It is requested that the Examiner fully consider the cited references and that they be cited on the front of any patent issuing from this application. An early action on the merits is respectfully requested.

Respectfully submitted,



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Date: 12-09-02
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PATENT TRADEMARK OFFICE

In place of PTO-1449 Form				U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				Application Number	Continuation of 10/120,872
				Filing Date	Herewith
				Applicant(s)	Kitsukawa, et al.
				Art Unit	Unknown
				Examiner Name	Unknown
SHEET	1	OF	1	Attorney Docket Number	5774.60

[illegible]

FOREIGN PATENT DOCUMENTS					
Examiner's Initials	Cite No.	Foreign Patent Document (Country Code - Number - Kind)	Publication Date MM/YY	Patentee or Applicant of Cited Document	Translation Y/N
	AI	EP 0239913 A2	07/1987	Fujitsu Ltd.	English
	AJ	EP 0281868 A2	09/1998	Nippon Electric Co.	English

OTHER PRIOR ART		
Examiner's Initials	Cite No.	*Include name of the author (in CAPITAL LETTERS); title of the article; title of the item; date; page(s); volume; issue number(s); publisher; city/country where published
	AK	NODA et al., A Boosted Dual Word-Line Decoding Scheme for 256 Mb DRAMs, pp. 112-113.
	AL	YAMADA et al., A 64-Mb DRAM with Meshed Power Line, November 1991, pp. 1506-1510
	AM	SUGIBAYASHI et al., A 30-ns 256 Mb DRAM with a Multidivided Array Structure, November 1993, pp. 1092-1098

Examiner Signature		Date Considered	
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Date
Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.